Electronic Engineer

Position Title:	Electronic Engineer
Position Reference Number:	ECRRSD007
Division	Research Services Division
Position Classification:	S&T 3- S&T 4 (APS 4/5-6)
Position Location:	Edinburgh SA
Security Level:	Negative Vetting 1/Negative Vetting Level 2
Enquiries:	DC Bandara, Dharmapriya.Bandara@dst.defence.gov.au , 08 7389 6785

Academic Disciplines

Aerospace/ Aeronautical Engineering, Naval Architecture	Chemical, Radiological, Biological, Food sciences	Materials Science
Computer Sciences, IT, Software Engineering, Telecommunications	Mathematics and physics	Psychology and Social Sciences
Mechanical and Mechatronic Engineering (including robotics)	Electronic/ Electrical Engineering	Other

Position Overview

Defence Science Technology's (DST) Scientific Engineering Services (SES) deliver specialised multi-disciplinary scientific engineering, design and fabrication solutions which contributes to maintaining DST's position at the forefront of Australian Defence innovation and National Security operations. SES fosters a culture of excellence, world leading capability and strong external partnerships dedicated to supporting the engineering needs of DST.

SES is seeking to engage a skilled and enthusiastic electronic engineer looking to develop a career in hosted and embedded electronic hardware and software sub-systems, working at the leading edge of engineering. The successful applicant will undertake project management, design development, review, manufacture and implementation activities and will collaborate across the spectrum of mechanical, electronic, micro-engineering and applied imaging design environments. The role is suitable for an engineer in the early stage of their career, with some experience across a broad range of Electronic Design skills and Electronic CAD (Mentor PADS Pro, Mentor Expedition or Altium Designer).

SES will consider part time applicants and strongly encourages both male and female engineers to apply. SES will provide and fund opportunities for further training, education and professional development to enhance skills in the domain of electronic systems design and development.

Position Duties

The Electronic Engineer will:
- Design and develop Embedded Microprocessor Based Digital Hardware Design; Embedded FPGA Based Logic Design; Printed Circuit Board Design; Analog Power Systems Design; Analog Signal Conditioning Design; Embedded Software Design (Contexts including General Purpose Operating System, RTOS, bare metal); and Hosted Software Design (C#/C++);
- Manage projects and prepare design documentation;
- Apply practical experience with embedded systems and/or microprocessors; with schematic capture and simulation tools; and with PCB design and layout for EMC, immunity, vibration and environmental impacts; and
- Produce, test and design verification reports and apply quality management principles with ISO9001 accredited quality systems.

Other Requirements

Positions may be exposed to data centre hazards, personal protection equipment will be provided. Some interstate travel and participation on scientific research trials may be required. Positions work as part of a dynamic team and there may be some peaks and troughs in the workload.

APPLY [HERE](#)*

IMPORTANT APPLICATION INSTRUCTIONS:

Applications are welcomed from candidates with **relevant qualifications** (ie, certificate, diploma, undergraduate, or post-graduate degrees). Please note to be successful at the S&T levels 3 and 4, **candidates will also require 2 or more years demonstrated industry experience,** and some positions require industry accreditations.

Please note: SES are making use of **DST's "Early Career Researcher"** application process for this recruitment activity. These positions are not research positions; they work alongside DST researchers toward scientific innovations and outcomes. **Please be assured this is the correct method of application despite the name of the process**.

For those applicants without a university degree, there is currently no 'non-degree' option on the application form under "**Applicant Preferences**" for the item relating to "**Education and relevant work experience**". If this is you, please select "**Graduate (Bachelor/Honours/Masters) with more than 2-years relevant work experience**" as the default, and be sure to include details as to your correct education, experience, and industry accreditation in your Resume. Thank you for your understanding while DST work with the Defence Careers team to correct this anomaly.